

Silicon PNP Power Transistors

2SA1746

DESCRIPTION

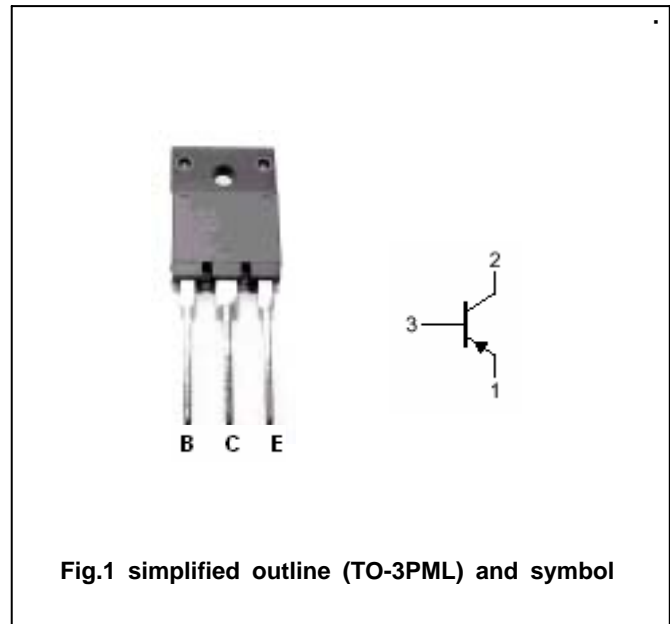
- With TO-3PML package
- Low collector saturation voltage

APPLICATIONS

- For chopper regulator, switch and general purpose applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector
3	Base

Absolute maximum ratings($T_c=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-70	V
V_{CEO}	Collector-emitter voltage	Open base	-50	V
V_{EBO}	Emitter-base voltage	Open collector	-6	V
I_C	Collector current		-12	A
I_{CM}	Collector current-peak		-20	A
I_B	Base current		-4	A
P_C	Collector power dissipation	$T_c=25$	60	W
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-25mA; I _B =0	-50			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-5 A; I _B =-80m A			-0.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-5 A; I _B =-80m A			-1.2	V
I _{CBO}	Collector cut-off current	V _{CB} =-70V; I _E =0			-10	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-6V; I _C =0			-10	μ A
h _{FE}	DC current gain	I _C =-5A ; V _{CE} =-1V	50			
f _T	Transition frequency	I _C =-1A ; V _{CE} =-12V		25		MHz
C _{OB}	Output capacitance	I _E =0; V _{CB} =-10V; f=1MHz		400		pF

Switching times

t _{on}	Turn-on time	I _C =-5A; R _L =4 I _{B1} =-I _{B2} =-80mA V _{CC} =-20V		0.5		μ s
t _s	Storage time			0.6		μ s
t _f	Fall time			0.3		μ s

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PACKAGE OUTLINE

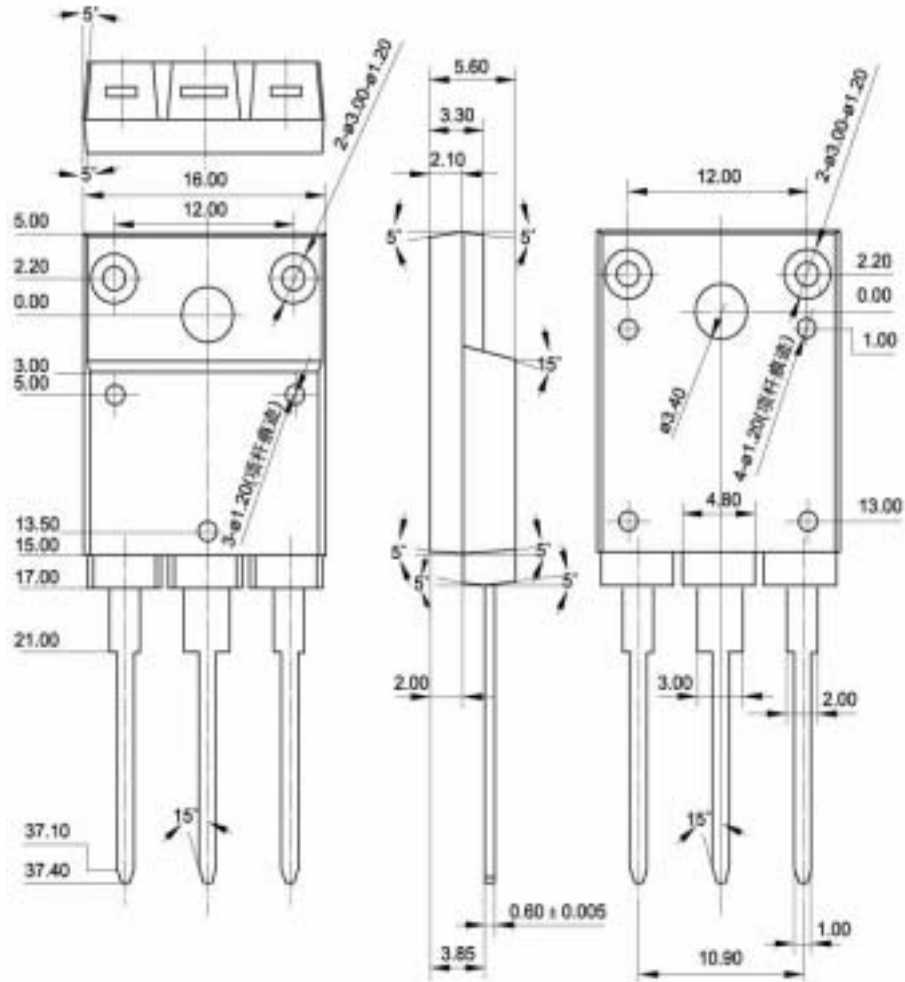


Fig.2 Outline dimensions

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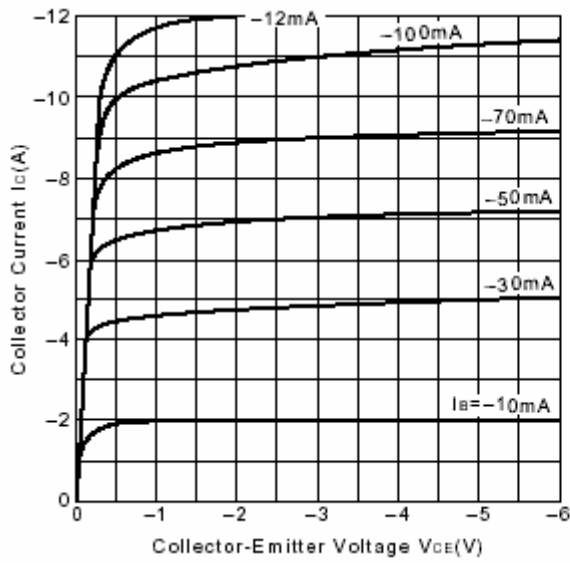


Fig.3 Static Characteristic

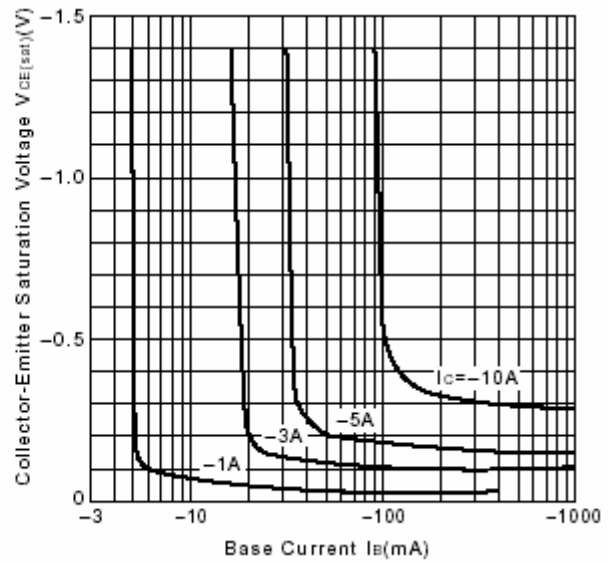


Fig.4 $V_{CE(sat)}-I_B$ Characteristics

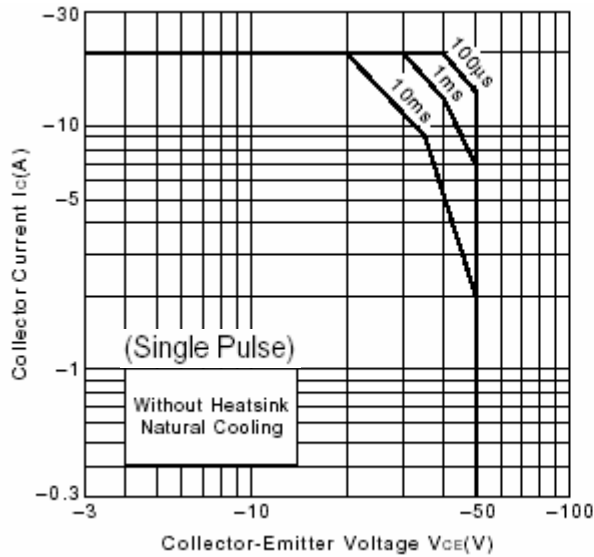


Fig.5 Safe Operating Area

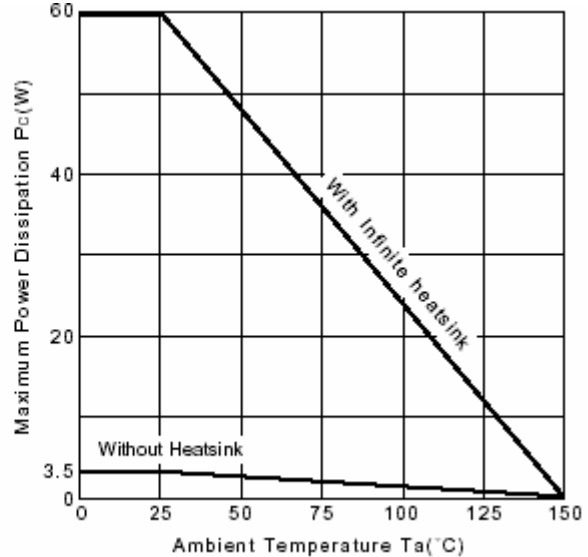


Fig.6 P_c-T_a Derating

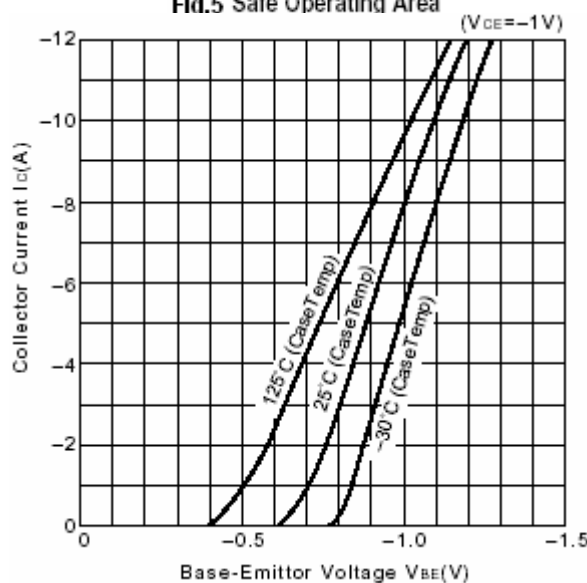


Fig.7 I_c-V_{BE}

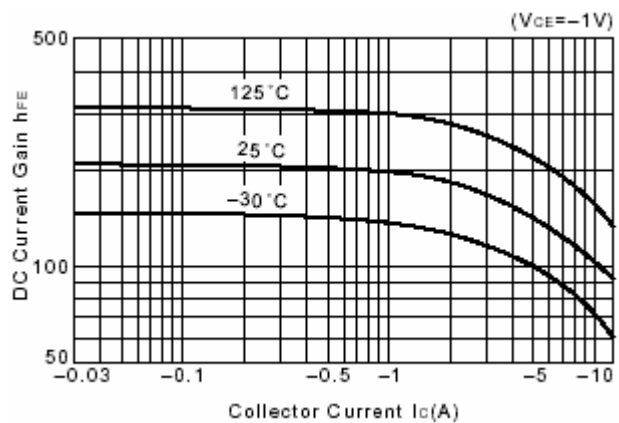


Fig.8 DC current Gain