

Silicon PNP Power Transistors

2SB548

DESCRIPTION

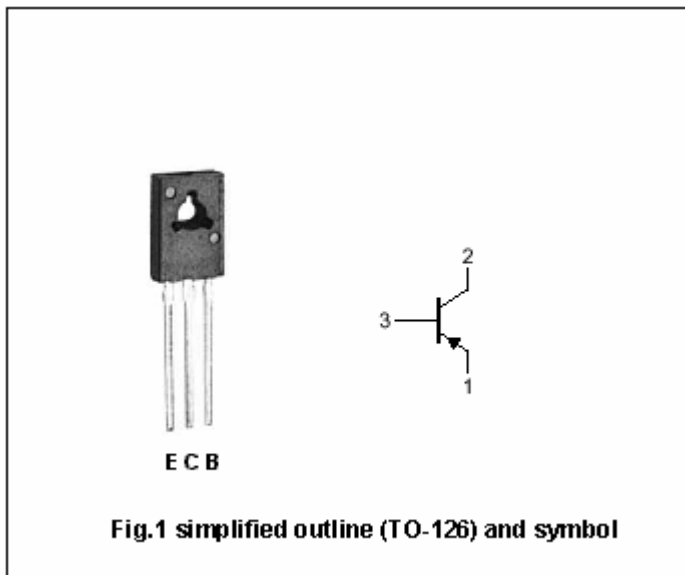
- With TO-126 package
- Complement to type 2SD414

APPLICATIONS

- Designed for use in driver and output stages of audio frequency amplifiers

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-100	V
V _{CEO}	Collector-emitter voltage	Open base	-80	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current (DC)		-0.8	A
P _D	Total power dissipation	T _a =25	1.0	W
		T _C =25	10	
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

Silicon PNP Power Transistors

2SB548

CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-10mA ; I _B =0	-80			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-50 μ A ; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-0.5A ; I _B =-50mA			-2.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-0.5A ; I _B =-50mA			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-80V ; I _E =0			-1.0	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-5V ; I _C =0			-1.0	μ A
h _{FE}	DC current gain	I _C =-0.2A ; V _{CE} =-5V	40		320	
C _{OB}	Output capacitance	I _E =0 ; V _{CB} =-10V ; f=1MHz		25		pF
f _T	Transition frequency	I _C =-0.1A ; V _{CE} =-5V		70		MHz

Silicon PNP Power Transistors

2SB548

PACKAGE OUTLINE

