

Silicon PNP Power Transistors

2SB557

DESCRIPTION

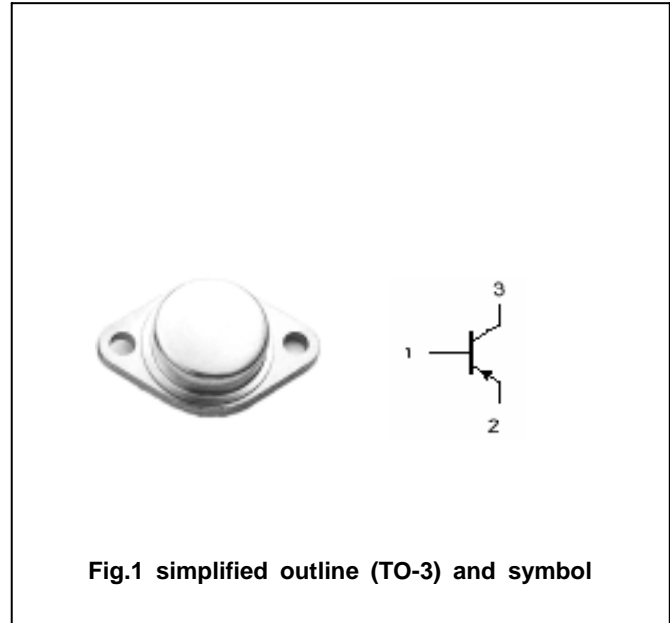
- With TO-3 package
- Complement to type 2SD427
- High power dissipation

APPLICATIONS

- Power amplifier applications
- Recommended for 50W high-fidelity audio frequency amplifier output stage

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings($T_a =$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-120	V
V_{CEO}	Collector-emitter voltage	Open base	-120	V
V_{EBO}	Emitter-base voltage	Open collector	-5	V
I_C	Collector current		-8	A
I_E	Emitter current		8	A
P_C	Collector power dissipation	$T_C=25$	80	W
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-65~150	

Silicon PNP Power Transistors

2SB557

CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-0.1A ; I _B =0	-120			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-10mA ; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-5A ; I _B =-0.5A			-2.5	V
V _{BE}	Base-emitter on voltage	I _C =-5A ; V _{CE} =-5V			-2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =-60V ; I _E =0			-0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V ; I _C =0			-0.1	mA
h _{FE-1}	DC current gain	I _C =-1A ; V _{CE} =-5V	40		140	
h _{FE-2}	DC current gain	I _C =-5A ; V _{CE} =-5V	20			
C _{OB}	Output capacitance	I _E =0 ; V _{CB} =-10V ; f=1.0MHz		280		pF
f _T	Transition frequency	I _C =-1A ; V _{CE} =-5V		7		MHz

◆ h_{FE-1} Classifications

R	O
40-80	70-140

Silicon PNP Power Transistors

2SB557

PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance: $\pm 0.1\text{mm}$)